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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/763,670

01/24/2004

Tai-Cheng Wang

04113-URS

7472

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7590

02/13/2006

SUPREME PATENT SERVICES

P.O. BOX 2339

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EXAMINER

VO, TIM T

ART UNIT

PAPER NUMBER

2112

DATE MAILED: 02/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/763,670	Applicant(s) WANG, TAI-CHENG	
	Examiner Tim T. Vo	Art Unit 2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 January 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5 and 9 is/are rejected.
- 7) ☒ Claim(s) 6-8 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 January 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

Part III DETAILED ACTION

Notice to Applicant(s)

This application has been examined. Claims 1-9 are pending.

Claim Rejections - 35 USC § 112

Claims 1-8 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

As per claim 1, the preamble indicated "a one-bus multimedia computer system". However, in the body of the claim, there are more than one bus is claimed, such as program and sound bus, graphic bus and one bus on the other side of the arbiter.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5 and 9 are rejected under 35 U.S.C. § **102(b)** as being anticipated by Ishida patent number 5,566,306 referred hereinafter "Ishida".

Art Unit: 2112

As for claims 1 and 9, Ishida teaches a one-bus multi-media computer system, comprising: a CPU/Sound/Graphic unit connected to a program and sound bus and a graphic bus (see figure 12, image play back device 1091, bus connecting to the system interface 1101 and column 12 lines 39-62, wherein the bus is transferring sound and graphics from the compressed image expander 1092 or from the buffer memory 105. Further the bus is connecting to the arbiter 106 and wherein the arbiter 106 is configured by programming (see column 12 lines 63-67)); a bus arbitrator connected to said program and sound bus and said graphic bus on one side (see figure 12, bus arbiter 106 connecting to the program and sound bus via the system interface 1101), and said one-bus on the other side (see figure 12, bus connecting to the buffer memory 105); a program and sound and graphic memory connected to said one-bus (see figure 12, bus connecting to the system interface 1101 and column 39-62); and a TV/LCD signal unit for outputting audio and video signals (see column 12 lines 56-62 and column 13 lines 1-6); said CPU/Sound/Graphic unit requests said program and sound and graphic memory by memory addresses (see column 8 line 66 to column 9 line 3), processes data returned from said program and sound and graphic memory, and sends the signals to said TV/LCD signal unit for outputting (see column 13 lines 7-21), said bus arbitrator sits between said CPU/Sound/Graphic and said program and sound and graphic memory to arbitrate said memory requests from said CPU/Sound/Graphic unit to said program and sound and graphic memory (see column 5 lines 50-64).

As for claim 2, Ishida teaches wherein said one-bus further comprises an address bus

Art Unit: 2112

for sending address and a data bus for sending data (see figure 12 bus connecting to the system interface 1101).

As for claim 3, Ishida teaches wherein said program and sound bus further comprises an address bus for sending address and a data bus for sending data (see figure 12 bus connecting to the system interface 1101).

As for claim 4, Ishida teaches wherein said graphic bus further comprises an address bus for sending address and a data bus for sending data (see figure 12).

As for claim 5, Ishida teaches wherein said bus arbitrator uses the rule that a memory request to a faster memory is given a higher priority to access the bus without the pre-emptive capability (see column 13 lines 15-16).

Allowable Subject Matter

Claims 6-8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Art Unit: 2112

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 571-272-3642.

The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on 571-272-3672. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

2/6/06



Tim T. Vo
Primary Examiner
Art Unit 2112